

HMALC-AS3-5252
HMALC-AS3-15252
HMALC-AS3-152152

General Description:

The HMALC-AS3 is designed to be used in conjunction with the **Hpe[®]_midiv2** . Two ALTERA Stratix III devices are the main part of this module which can be used for the functional verification of high complex digital circuits and for SEmulation. The connection to the main board is done by two 132 pin Elastomer module connectors.

The original ALTERA USB Blaster is already on board. A clock factory make it easy to distribute different clock signals to the FPGA.

Together with this module we deliver our Hpe_desk. This is a system handling software which includes the clock factory programmer, a JTAG debugger and scanner and some board data will be displayed.



Functional Description:

The following features are the most important parts of this FPGA module:

- Two 780 pin ALTERA[®] Stratix III FPGA EP3S50-C2 or EP3S150-C2 (DUT#1 and DUT#2)
- 28 LVDS pairs and 125 single ended signals connects the two FPGA together
- Interconnection designed for highest possible speed
- Two 132 pin Elastomer module connectors (X1, X2) connects the module with the Hpe[®]_midiv2 main board.
- One 132 pin Elastomer Hpe Child board connectors (X3) for DDR2-SDRAMs or any other Hpe[®]_child board
- Common Flash for the configuration of both FPGA
- 2 SSRAM for high speed processor RAM
- Big Flash for CPU program
- CPLD for clock distribution (clock factory), configuration and fan control (PWM)
- CPLD for FPGA Code Protection (FCP) and ALTERA USB blaster
- Advanced Encryption Standard (AES), programmed on customer request
- 2 SEmulator connectors for connecting external SEmulator box.
- 2 RS232 on 10 pin flat ribbon connectors
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- **Module can be used together with Hpe_midiv2 or stand-alone.**

Order code:

Bord Type	FPGA1	FPGA2
HMALC-AS3-5252	EP3S50-C2	EP3S50-C2
HMALC-AS3-15252	EP3S150-C2	EP3S50-C2
HMALC-AS3-152152	EP3S150-C2	EP3S150-C2

The core current of both FPGA is limited to 8A. External supply is possible.

Block Diagram:

